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Kinetis Peripheral Module Quick Reference, Rev. 0 2 Freescale Semiconductor Revision History Date Revision Level Description Page Number(s) 11/2010 0 Initial release N/A

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## **KEA128LEDLIGHTRD Quick Start Guide**

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Description The KwikStik development board features the MK40X256VLQ100—an ARM Cortex-M4 based microcontroller with segment LCD and USB 2.0 full-speed OTG controllers in a 144 LQFP package with a maximum core operating frequency of 100MHz. ...

### **KwikStik-K40**

Quick Start Guide 3 Run Example Project a. Start IAR Embedded Workbench and click “EXAMPLE PROJECTS” in IAR Information Center. b. Select Freescale > Freescale Kinetis > K60 > Freescale TWR-K60 board, then click button to open the project. Choose a destination folder to save a copy of this project. c. a.Click button to build the project, then

### **IAR KickStart Kit Quick Start Guide**

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### **How to Make a Quick-Reference Guide (With Templates ...**

TWRK60D100MUM TWR-K60N512 Tower Module User's Manual Page 6 of 18 Figure 3. Callouts on back side of the TWR-K60D100M 1.3 Getting Started Follow the Quick Start Guide found printed in the TWR-K60D100M box or on the web for the list of recommended steps for getting started.

### **TWR K60D100M Tower**

Kinetis Quick Reference User Guide (QRUG) Tower Configuration Tool 2 Hardware Description The TWR-K60N512 is a Tower Controller Module featuring the MK60N512VMD100—an ARM Cortex-M4 based microcontroller with segment LCD and USB 2.0 full-speed OTG controllers in a 144 MAPBGA

## **TWR-K60N512 Tower Module**

Users Guide KLQRUG Rev. 0, 09/2012 This collection of code examples, useful tips, and quick reference material has been created to help you speed the development of your applications. Most chapters in this document contain examples that can be modified to work with Kinetis MCU Family members. When you're

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Quick Start Guide TWR-K60F120M Features • MK60FN1M0VLQ12 MCU (120 MHz ARM® Cortex™-M4 core, floating point unit, 1 MB flash, Ethernet, USB OTG, tamper detection, encryption, NAND flash controller, 144 LQFP) • MC9S08JM60 open source JTAG (OSJTAG) circuit • Micron MT29F2G16ABAEAWP 2 Gb NAND flash • Four user-controlled status LEDs

## **Quick Start Guide - NXP**

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